Interior and interface between front-END SYSTEMS AND BACK-END SYSTEMS

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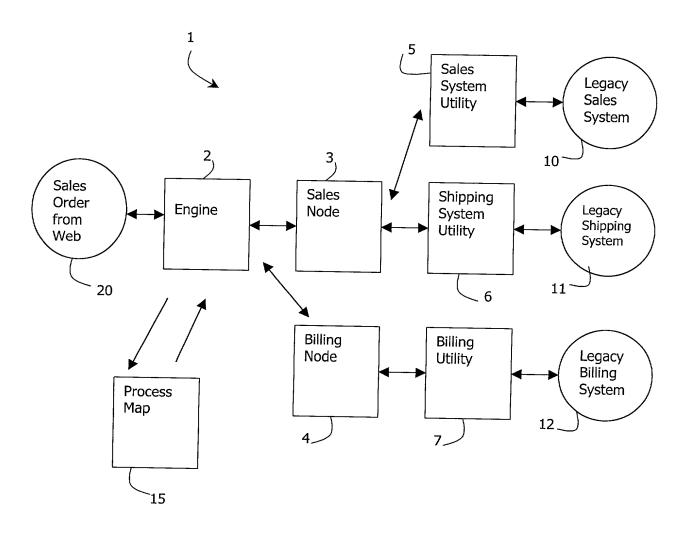


Fig. 1